

< HVIGBT MODULES >

# CM750HG-130R

HIGH POWER SWITCHING USE  
INSULATED TYPE

4th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

## CM750HG-130R



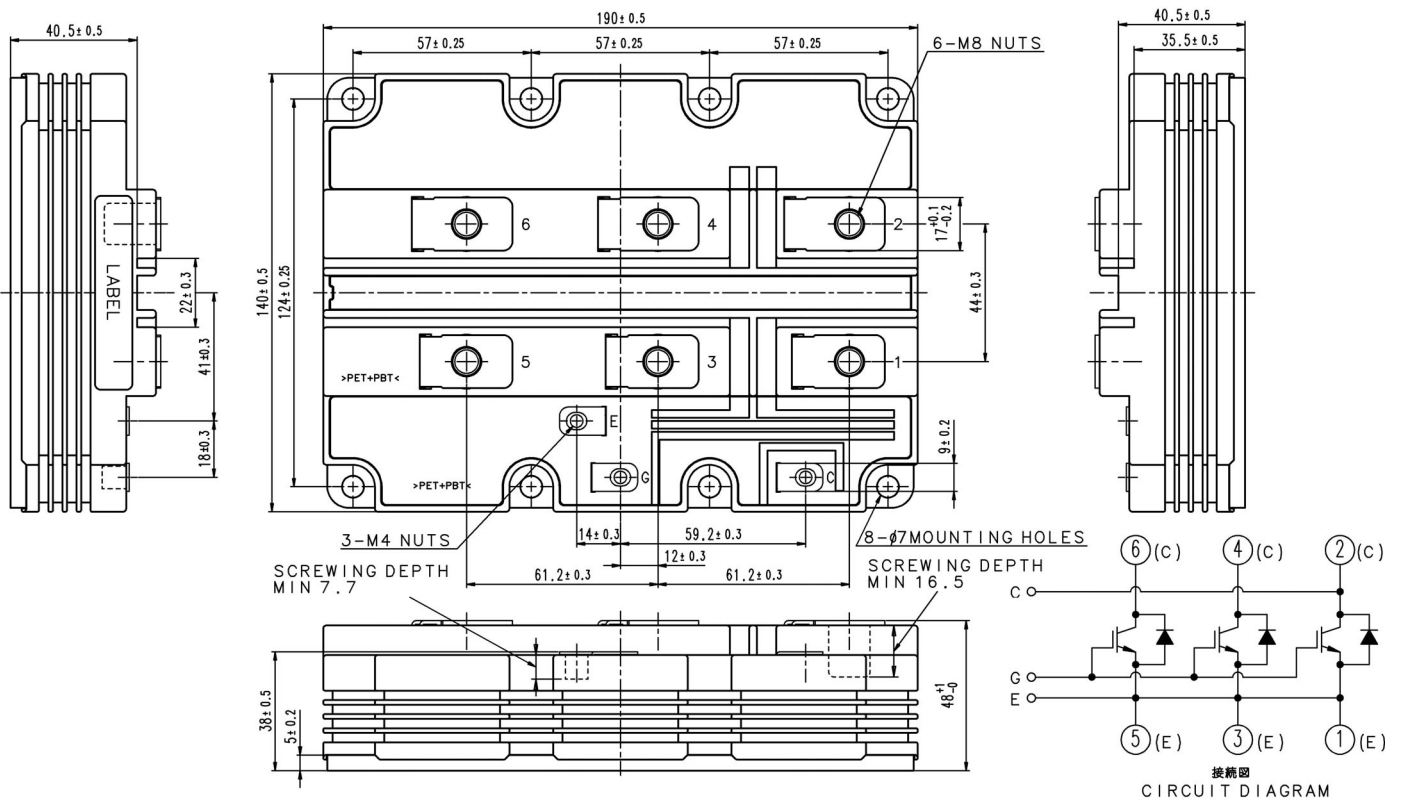
- $I_C$  ..... 750A
- $V_{CES}$  ..... 6500V
- 1-element in a pack
- High Insulated type
- LPT-IGBT / Soft Recovery Diode
- AISiC baseplate

## APPLICATION

Traction drives, High Reliability Converters / Inverters, DC choppers

## OUTLINE DRAWING & CIRCUIT DIAGRAM

Dimensions in mm



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**MAXIMUM RATINGS**

Symbol	Item	Conditions	Ratings	Unit
V <sub>CES</sub>	Collector-emitter voltage	V <sub>GE</sub> = 0V, T <sub>j</sub> = +125°C	6500	V
		V <sub>GE</sub> = 0V, T <sub>j</sub> = +25°C	6300	
		V <sub>GE</sub> = 0V, T <sub>j</sub> = -50°C	5700	
V <sub>GES</sub>	Gate-emitter voltage	V <sub>CE</sub> = 0V, T <sub>j</sub> = 25°C	± 20	V
I <sub>C</sub>	Collector current	DC, T <sub>c</sub> = 95°C	750	A
I <sub>CRM</sub>		Pulse (Note 1)	1500	A
I <sub>E</sub>	Emitter current (Note 2)	DC	750	A
I <sub>ERM</sub>		Pulse (Note 1)	1500	A
P <sub>tot</sub>	Maximum power dissipation (Note 3)	T <sub>c</sub> = 25°C, IGBT part	10400	W
V <sub>iso</sub>	Isolation voltage	RMS, sinusoidal, f = 60Hz, t = 1 min.	10200	V
V <sub>e</sub>	Partial discharge extinction voltage	RMS, sinusoidal, f = 60Hz, Q <sub>PD</sub> ≤ 10 pC	5100	V
T <sub>j</sub>	Junction temperature		-50 ~ +150	°C
T <sub>jop</sub>	Operating junction temperature		-50 ~ +125	°C
T <sub>stg</sub>	Storage temperature		-55 ~ +125	°C
t <sub>psc</sub>	Short circuit pulse width	V <sub>CC</sub> = 4500V, V <sub>CE</sub> ≤ V <sub>CES</sub> , V <sub>GE</sub> = 15V, T <sub>j</sub> = 125°C	10	μs

**ELECTRICAL CHARACTERISTICS**

Symbol	Item	Conditions	Limits			Unit	
			Min	Typ	Max		
I <sub>CES</sub>	Collector cutoff current	V <sub>CE</sub> = V <sub>CES</sub> , V <sub>GE</sub> = 0V T <sub>j</sub> = 25°C T <sub>j</sub> = 125°C	—	—	24.0	mA	
V <sub>GE(th)</sub>	Gate-emitter threshold voltage	V <sub>CE</sub> = 10 V, I <sub>C</sub> = 75 mA, T <sub>j</sub> = 25°C	5.8	6.3	6.8	V	
I <sub>GES</sub>	Gate leakage current	V <sub>GE</sub> = V <sub>GES</sub> , V <sub>CE</sub> = 0V, T <sub>j</sub> = 25°C	-0.5	—	0.5	μA	
C <sub>ies</sub>	Input capacitance	V <sub>CE</sub> = 10 V, V <sub>GE</sub> = 0 V, f = 100 kHz T <sub>j</sub> = 25°C	—	136.0	—	nF	
C <sub>oes</sub>	Output capacitance		—	8.6	—	nF	
C <sub>res</sub>	Reverse transfer capacitance		—	4.0	—	nF	
Q <sub>G</sub>	Total gate charge	V <sub>CC</sub> = 3600V, I <sub>C</sub> = 750A, V <sub>GE</sub> = ±15V	—	10.5	—	μC	
V <sub>CESat</sub>	Collector-emitter saturation voltage	I <sub>C</sub> = 750 A (Note 4) V <sub>GE</sub> = 15 V	T <sub>j</sub> = 25°C	—	3.80	—	V
			T <sub>j</sub> = 125°C	—	4.80	5.60	
t <sub>d(on)</sub>	Turn-on delay time	V <sub>CC</sub> = 3600 V	T <sub>j</sub> = 25°C	—	1.05	—	μs
			T <sub>j</sub> = 125°C	—	1.00	1.80	
t <sub>r</sub>	Turn-on rise time	I <sub>C</sub> = 750 A V <sub>GE</sub> = ±15 V	T <sub>j</sub> = 25°C	—	0.18	—	μs
			T <sub>j</sub> = 125°C	—	0.20	0.50	
E <sub>on(10%)</sub>	Turn-on switching energy (Note 5)	R <sub>G(on)</sub> = 3.3 Ω L <sub>s</sub> = 150 nH	T <sub>j</sub> = 25°C	—	3.35	—	J
			T <sub>j</sub> = 125°C	—	4.10	—	
E <sub>on</sub>	Turn-on switching energy (Note 6)	Inductive load	T <sub>j</sub> = 25°C	—	3.50	—	J
			T <sub>j</sub> = 125°C	—	4.40	—	
t <sub>d(off)</sub>	Turn-off delay time	V <sub>CC</sub> = 3600 V	T <sub>j</sub> = 25°C	—	7.60	—	μs
			T <sub>j</sub> = 125°C	—	8.00	9.20	
t <sub>f</sub>	Turn-off fall time	I <sub>C</sub> = 750 A V <sub>GE</sub> = ±15 V	T <sub>j</sub> = 25°C	—	0.40	—	μs
			T <sub>j</sub> = 125°C	—	0.45	1.00	
E <sub>off(10%)</sub>	Turn-off switching energy (Note 5)	R <sub>G(off)</sub> = 33 Ω L <sub>s</sub> = 150 nH	T <sub>j</sub> = 25°C	—	3.10	—	J
			T <sub>j</sub> = 125°C	—	4.60	—	
E <sub>off</sub>	Turn-off switching energy (Note 6)	Inductive load	T <sub>j</sub> = 25°C	—	3.40	—	J
			T <sub>j</sub> = 125°C	—	4.90	—	

**ELECTRICAL CHARACTERISTICS (continuation)**

Symbol	Item	Conditions	Limits			Unit	
			Min	Typ	Max		
$V_{EC}$	Emitter-collector voltage (Note 2)	$I_E = 750 \text{ A}$ (Note 4) $V_{GE} = 0 \text{ V}$	$T_j = 25^\circ\text{C}$	—	3.30	—	V
			$T_j = 125^\circ\text{C}$	—	3.40	4.20	
$t_{rr}$	Reverse recovery time (Note 2)		$T_j = 25^\circ\text{C}$	—	0.65	—	$\mu\text{s}$
			$T_j = 125^\circ\text{C}$	—	0.70	—	
$I_{rr}$	Reverse recovery current (Note 2)	$V_{CC} = 3600 \text{ V}$ $I_C = 750 \text{ A}$	$T_j = 25^\circ\text{C}$	—	800	—	A
			$T_j = 125^\circ\text{C}$	—	900	—	
$Q_{rr}$	Reverse recovery charge (Note 2)	$V_{GE} = \pm 15 \text{ V}$ $R_{G(on)} = 3.3 \Omega$	$T_j = 25^\circ\text{C}$	—	630	—	$\mu\text{C}$
			$T_j = 125^\circ\text{C}$	—	900	—	
$E_{rec(10\%)}$	Reverse recovery energy (Note 2) (Note 5)	$L_s = 150 \text{ nH}$ Inductive load	$T_j = 25^\circ\text{C}$	—	0.90	—	J
			$T_j = 125^\circ\text{C}$	—	1.70	—	
$E_{rec}$	Reverse recovery energy (Note 2) (Note 6)		$T_j = 25^\circ\text{C}$	—	1.00	—	J
			$T_j = 125^\circ\text{C}$	—	1.80	—	

**THERMAL CHARACTERISTICS**

Symbol	Item	Conditions	Limits			Unit
			Min	Typ	Max	
$R_{th(j-c)Q}$	Thermal resistance	Junction to Case, IGBT part	—	—	12.0	K/kW
$R_{th(j-c)D}$		Junction to Case, FWDi part	—	—	22.0	K/kW
$R_{th(c-s)}$	Contact thermal resistance	Case to heat sink, $\lambda_{grease} = 1\text{W/m}^2\text{K}$ , $D_{(c-s)} = 100\mu\text{m}$	—	6.0	—	K/kW

**MECHANICAL CHARACTERISTICS**

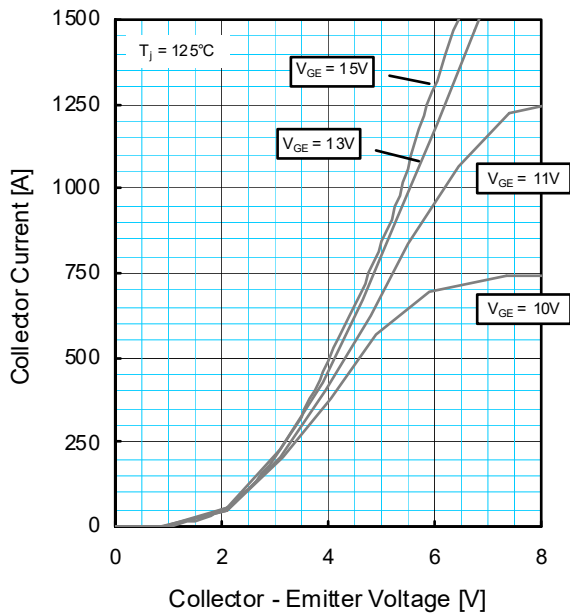
Symbol	Item	Conditions	Limits			Unit
			Min	Typ	Max	
$M_t$	Mounting torque	M8 : Main terminals screw	7.0	—	22.0	N·m
$M_s$		M6 : Mounting screw	3.0	—	6.0	N·m
$M_t$		M4 : Auxiliary terminals screw	1.0	—	3.0	N·m
$m$	Mass		—	1.4	—	kg
CTI	Comparative tracking index		600	—	—	—
$d_a$	Clearance		26.0	—	—	mm
$d_s$	Creepage distance		56.0	—	—	mm
$L_{PCE}$	Parasitic stray inductance		—	15.0	—	nH
$R_{CC+EE}$	Internal lead resistance	$T_C = 25^\circ\text{C}$	—	0.18	—	m $\Omega$
$r_g$	Internal gate resistance	$T_C = 25^\circ\text{C}$	—	2.6	—	$\Omega$

Note1. Pulse width and repetition rate should be such that junction temperature ( $T_j$ ) does not exceed  $T_{jopmax}$  rating.

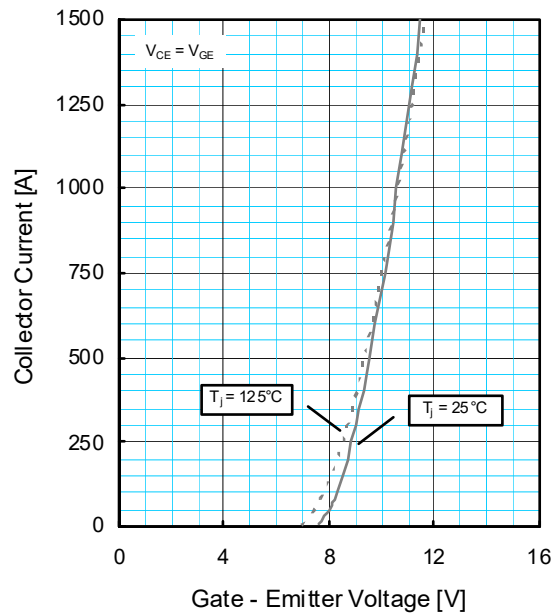
- The symbols represent characteristics of the anti-parallel, emitter to collector free-wheel diode (FWD).
- Junction temperature ( $T_j$ ) should not exceed  $T_{jmax}$  rating ( $150^\circ\text{C}$ ).
- Pulse width and repetition rate should be such as to cause negligible temperature rise.
- $E_{on(10\%)}$  /  $E_{off(10\%)}$  /  $E_{rec(10\%)}$  are the integral of  $0.1V_{CE} \times 0.1I_C \times dt$ .
- Definition of all items is according to IEC 60747, unless otherwise specified.

PERFORMANCE CURVES

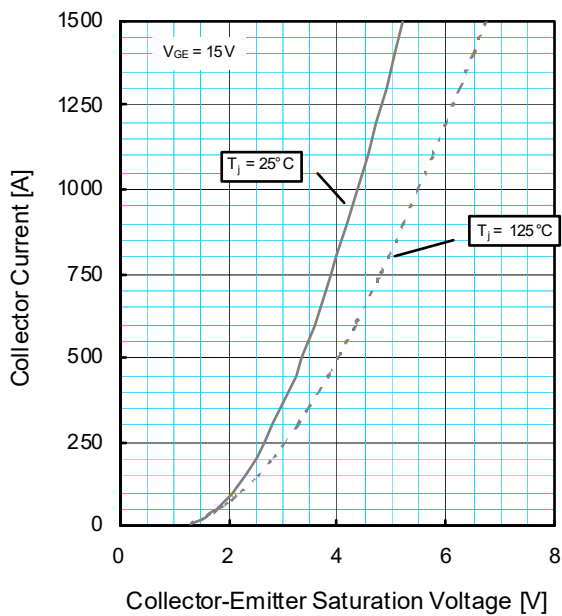
**OUTPUT CHARACTERISTICS (TYPICAL)**



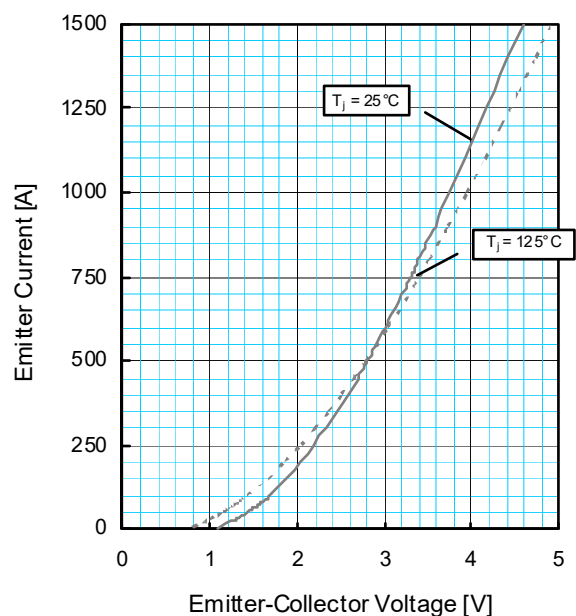
**TRANSFER CHARACTERISTICS (TYPICAL)**



**COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)**

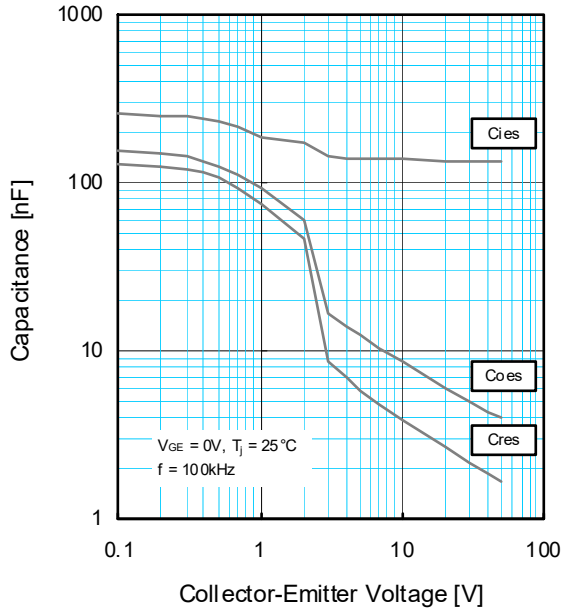


**FREE-WHEEL DIODE FORWARD CHARACTERISTICS (TYPICAL)**

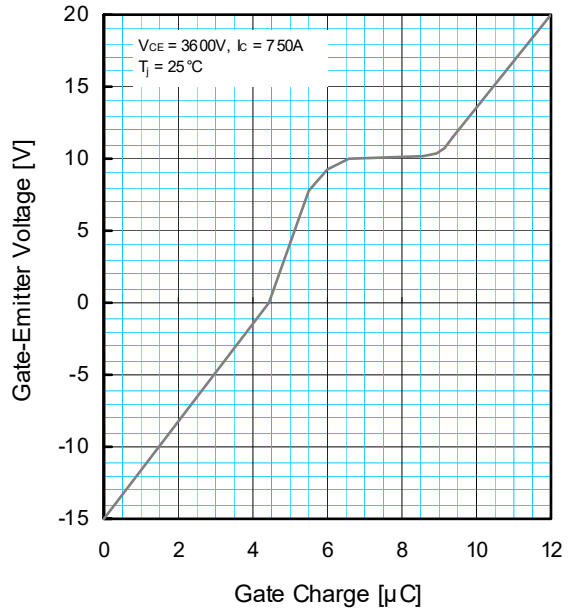


PERFORMANCE CURVES

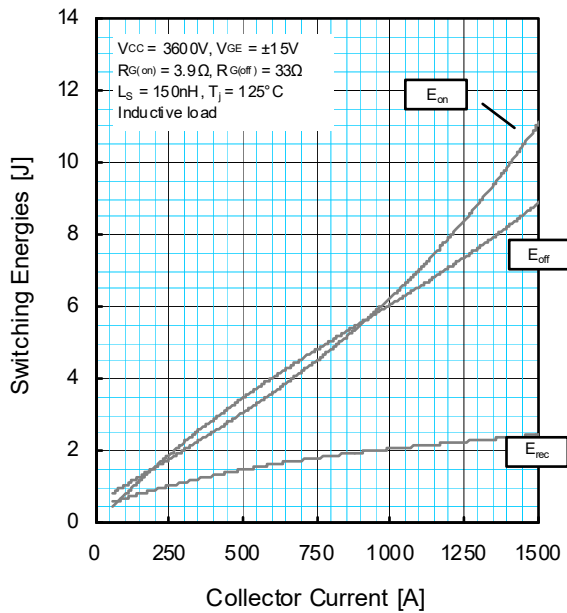
**CAPACITANCE CHARACTERISTICS (TYPICAL)**



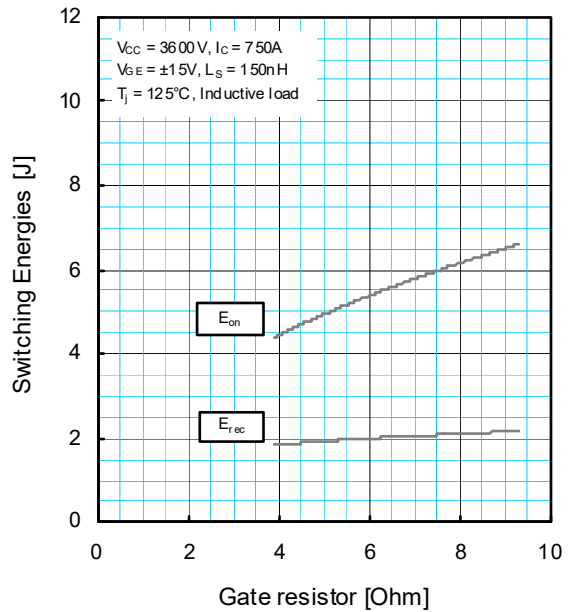
**GATE CHARGE CHARACTERISTICS (TYPICAL)**



**HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)**

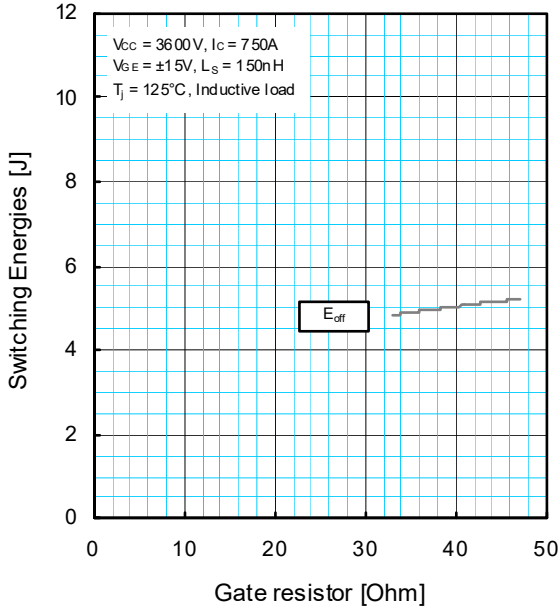


**HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)**

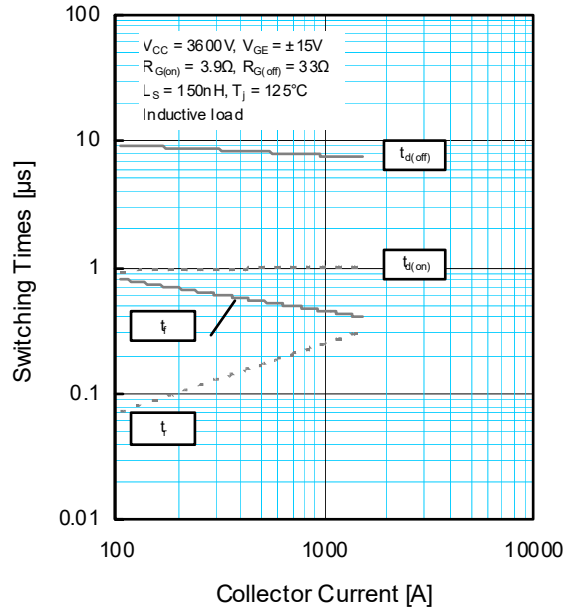


PERFORMANCE CURVES

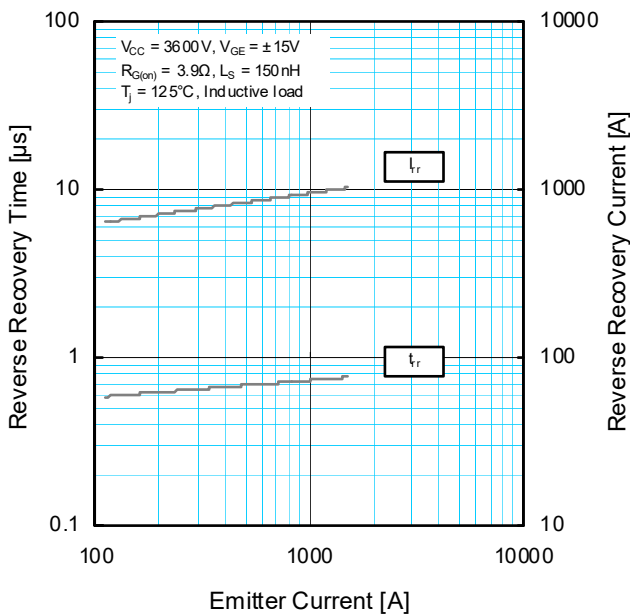
**SWITCHING ENERGY CHARACTERISTICS (TYPICAL)**



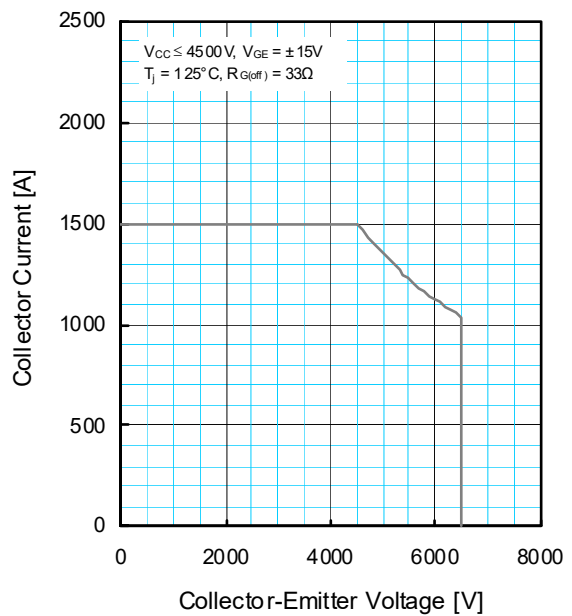
**HALF-BRIDGE SWITCHING TIME CHARACTERISTICS (TYPICAL) HALF-BRIDGE**



**FREE-WHEEL DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)**

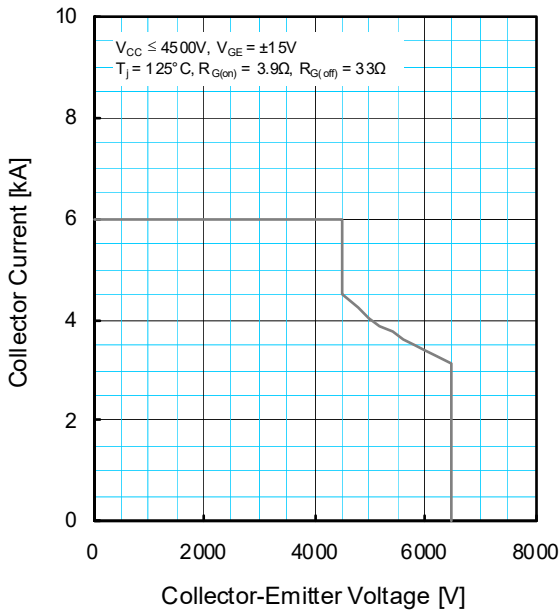


**REVERSE BIAS SAFE OPERATING AREA (RBSOA)**

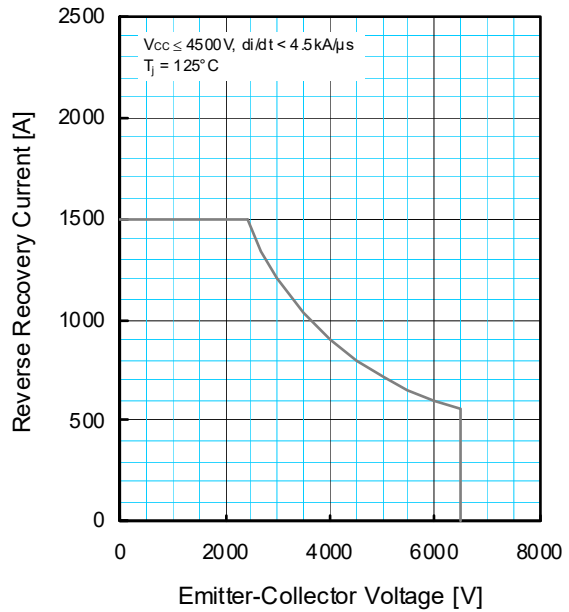


PERFORMANCE CURVES

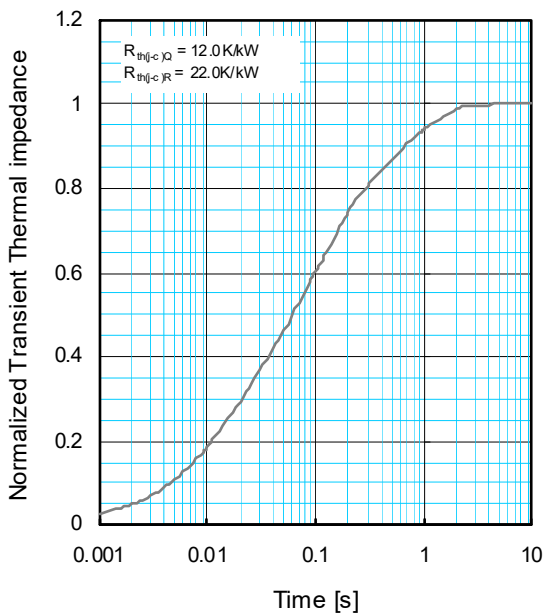
**SHORT CIRCUIT  
SAFE OPERATING AREA (SCSOA)**



**FREE-WHEEL DIODE REVERSE RECOVERY  
SAFE OPERATING AREA (RRSOA)**



**TRANSIENT THERMAL IMPEDANCE  
CHARACTERISTICS**



$$Z_{th(j-c)}(t) = \sum_{i=1}^n R_i \left\{ 1 - \exp\left(-\frac{t}{\tau_i}\right) \right\}$$

	1	2	3	4
$R_i$ [K/kW] :	0.0055	0.2360	0.4680	0.2905
$\tau_i$ [sec] :	0.0001	0.0131	0.0878	0.6247

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