Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 **Renesas Electronics Corporation**

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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2SK1153, 2SK1154 Silicon N Channel MOS FET

REJ03G0908-0200 (Previous: ADE-208-1246) Rev.2.00 Sep 07, 2005

Application

High speed power switching

Features

- Low on-resistance
- High speed switching
- Low drive current •
- No secondary breakdown
- Suitable for switching regulator and DC-DC converter

Outline

oduci RENESAS Package code: PRSS0004AC-A (Package name: TO-220AB) D 1. Gate 2. Drain (Flange) 3. Source ς



Absolute Maximum Ratings

				$(Ta = 25^{\circ}C)$	
ltem		Symbol	Ratings	Unit	
Drain to source voltage	2SK1153	V _{DSS}	450	V	
	2SK1154		500		
Gate to source voltage		V _{GSS}	±30	V	
Drain current		I _D	3	А	
Drain peak current		I _{D(pulse)} * ¹	12	A	
Body to drain diode reverse drain current		I _{DR}	3	А	
Channel dissipation		Pch* ²	30	W	
Channel temperature		Tch	150	°C	
Storage temperature		Tstg	-55 to +150	°C	

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

2. Value at $T_C = 25^{\circ}C$

Electrical Characteristics



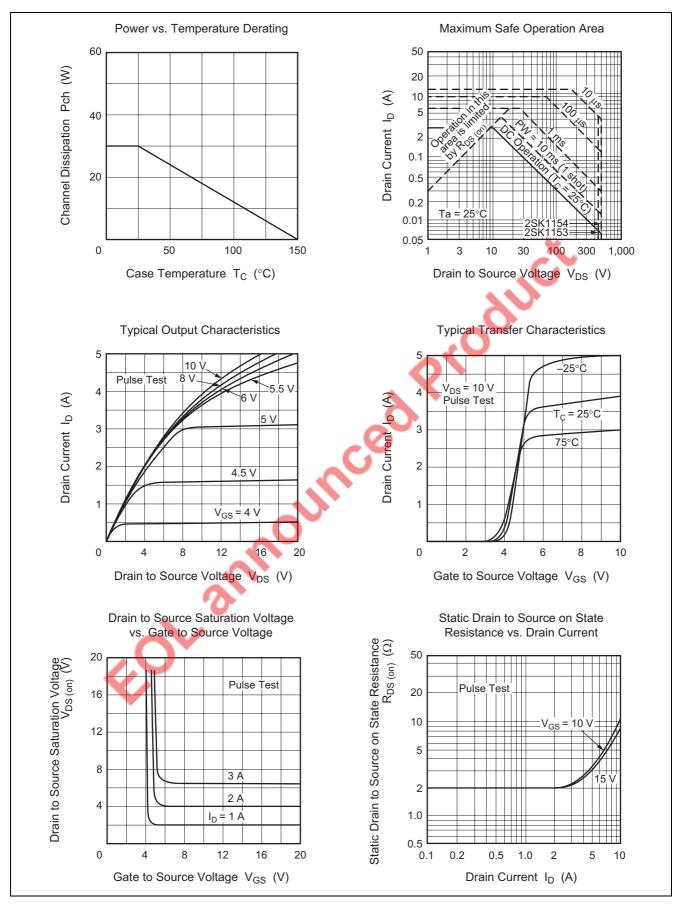
 $(Ta = 25^{\circ}C)$

Item		Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source	2SK1153	V _{(BR)DSS}	450	_	—	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
breakdown voltage	2SK1154		500				
Gate to source breakdown voltage		V _{(BR)GSS}	±30	_	Θ	V 🗸	$I_G = \pm 100 \ \mu A, V_{DS} = 0$
Gate to source leak current		I _{GSS}	—	- 4	±10	μA	$V_{GS} = \pm 25 \text{ V}, V_{DS} = 0$
Zero gate voltage drain	2SK1153	I _{DSS}	—	- /	250	μΑ	$V_{DS} = 360 \text{ V}, \text{ V}_{GS} = 0$
current	2SK1154						$V_{DS} = 400 V, V_{GS} = 0$
Gate to source cutoff voltage		V _{GS(off)}	2.0	20	3.0	V	$I_D = 1 \text{ mA}, V_{DS} = 10 \text{ V}$
Static drain to source on	2SK1153	R _{DS(on)}	-	2.0	2.8	Ω	$I_D = 2 \text{ A}, V_{GS} = 10 \text{ V}^{*3}$
state resistance	2SK1154			2.2	3.0		
Forward transfer admittance		y _{fs}	1.5	2.5	—	S	$I_D = 2 \text{ A}, V_{DS} = 10 \text{ V}^{*3}$
Input capacitance		Ciss 🥖		330	—	pF	$V_{DS} = 10 V, V_{GS} = 0,$
Output capacitance		Coss)_	90	—	pF	f = 1 MHz
Reverse transfer capacitance		Crss	—	15	—	pF	
Turn-on delay time		t _{d(on)}	_	7	_	ns	$I_D = 2 \text{ A}, V_{GS} = 10 \text{ V},$
Rise time		t _r	_	20	_	ns	$R_L = 15 \Omega$
Turn-off delay time		t _{d(off)}	—	30	—	ns	
Fall time		t _f		20	_	ns	
Body to drain diode forward voltage		V _{DF}		0.9	_	V	$I_F = 3 A, V_{GS} = 0$
Body to drain diode reverse recovery time		t _{rr}	—	300	—	ns	$I_F = 3 \text{ A}, V_{GS} = 0,$ $di_F/dt = 100 \text{ A}/\mu \text{s}$

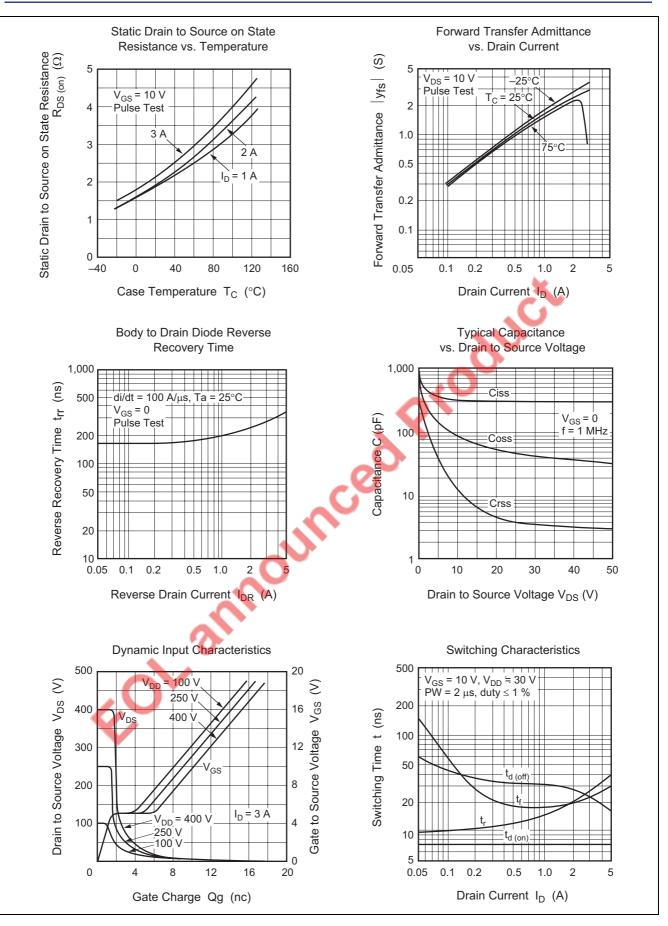
Note: 3. Pulse test



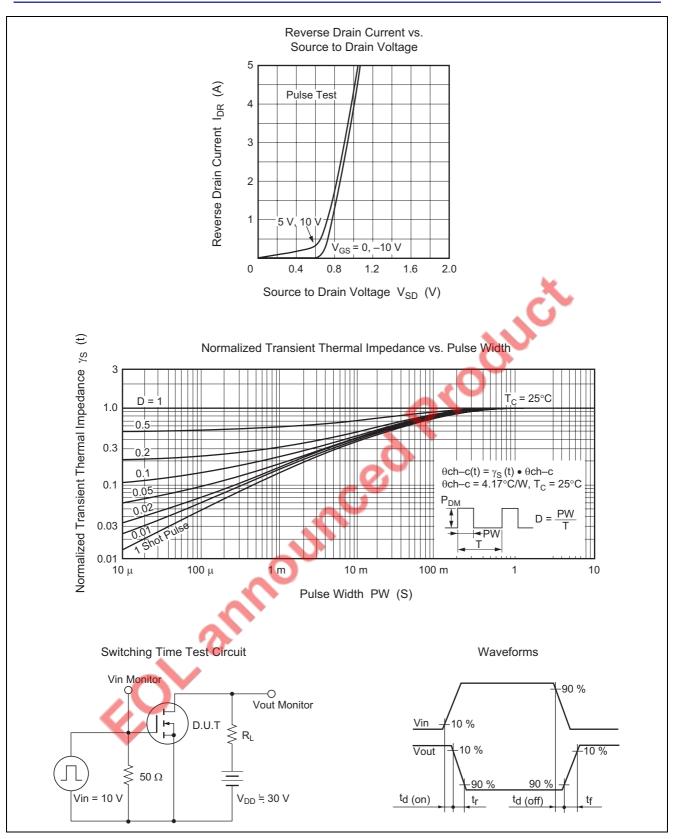
Main Characteristics



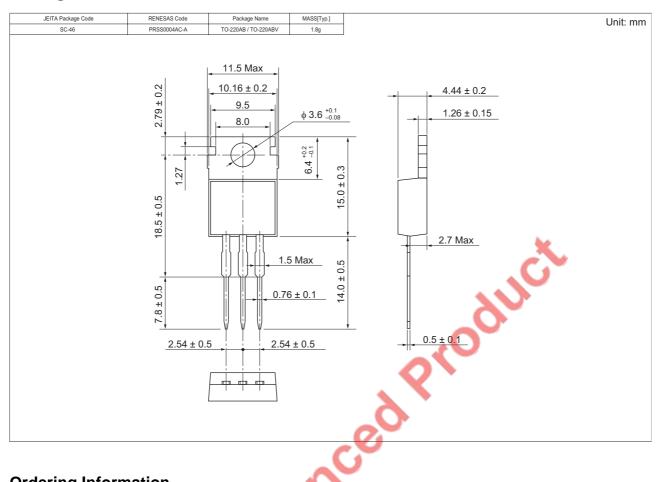








Package Dimensions

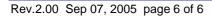


Ordering Information

1^C

Part Name	Quantity	5	Shipping Container
2SK1153-E	500 pcs 🧹		Box (Sack)
2SK1154-E	500 pcs 🛛 🔨		Box (Sack)

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.





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